

Electronic Filing System (EFS) Data

Electronic Patent Application Submission

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EFS ID: 14144
Application ID: 09683968
Title of Invention: ESD-Isolation Circuit Driving Gate
of Bus-Switch Transistor During
ESD Pulse Between Two I/O Pins
First Named Inventor: David Kwong
Domestic/Foreign Application: Domestic Application
Filing Date: null
Effective Receipt Date: 2002-03-07 
Submission Type: Utility Patent Filing
Filing Type: null
Confirmation Number: 0
Attorney Docket Number: PS-70
Digital Certificate Holder: cn=Stuart Thomas Auvinen, ou=Registered Attorneys, ou=Patent
and Trademark Office, ou=Department of Commerce, o=U.S.
Government, c=US
Certificate Message Digest: 0oa4yUEsI1tee6PijbrB2Q==
Total Fees Authorized: \$370.0

Payment Category: DA – Deposit Account
Deposit Account Number: 12950
Deposit Account Name: Stuart T Auvinen

TRANSMITTAL FORM

USPTO
09/683968
JC474 03/07/02

Electronic Version 1.0.3

Stylesheet Version: 1.0

Attorney Docket
Number:

PS-
70

Submission Type: Utility Patent
Filing

ESD-Isolation Circuit Driving Gate of Bus-Switch Transistor During ESD Pulse Between Two I/O Pins

First Named Inventor: David Kwong

SUBMITTED BY

Name: Stuart T Auvinen
Registration Number: 36,435
Electronic Signature
Mark: /e/stuauvinen
Date Signed: 20020307

I certify that the use of this system is for OFFICIAL correspondence between patent applicants or their representatives and the USPTO. Fraudulent or other use besides the filing of official correspondence by authorized parties is strictly prohibited, and subject to a fine and/or imprisonment under applicable law.

I, the undersigned, certify that I have viewed a display of document(s) being electronically submitted to the United States Patent and Trademark Office, using either the USPTO provided style sheet or software, and that this is the document(s) I intend for initiation or further prosecution of a patent application noted in the submission. This document(s) will become part of the official electronic record at the USPTO.

Attached Files:

declaration OathPS70.tif
bibd-transmittal PS70exapds.xml

fee-transmittal
specification

PS70exfee.xml

PS70e.xml

Attached Image File(s):

OathPS70.tif

Comments:

Please give the the Examiner: An IDS form with patent references will be filed with this case after the filing receipt is returned. Examiner is asked to call Stuart Auvinen at 831-476-5505 if the IDS is not in the file when examination starts and the IDS will be faxed to the Examiner.

DECLARATION FOR PATENT APPLICATION

PS-70

As a below-named inventor, I hereby declare that

My residence, mailing address, and citizenship are as stated below next to my name.

I believe that I am an original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention, the specification of which is attached hereto and which has the following title:

ESD-Isolation Circuit Driving Gate of Bus-Switch Transistor During ESD Pulse Between Two I/O Pins

I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment specifically referred to in the oath of declaration. I acknowledge a duty to disclose information which is material to examination of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56(a).

I hereby appoint the following Agent to prosecute this application and to transact all business in the Patent and Trademark Office connected herewith:

Stuart T. Auvinen, Reg. No. 36,435

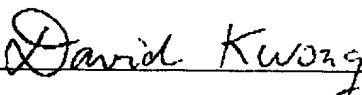
Address all telephone calls to Stuart T. Auvinen at telephone No. (831) 476-5506.

Address all correspondence to:

Stuart T. Auvinen
429 Twenty-Sixth Ave.
Santa Cruz, CA 95062

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Title 18, United States Code, Section 1001, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Joint/First Inventor's Signature



date: 3/6/2002

Full name of first inventor:

David Kwong

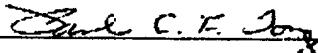
Residence: Fremont, CA

Citizen of: USA

Mailing Address:

44071 Linda Vista Rd, Fremont, CA 94539

Joint/Second Inventor's Signature



date: 3/6/02

Full name of second inventor: Paul C.F. Tong

Residence: San Jose, CA

Citizen of: USA

Mailing Address:

1545 Valley Crest Drive, San Jose, CA 95131

FEE TRANSMITTAL

Electronic Version 1.1.0

Stylesheet Version: 1.0

Patent fees are subject to annual revisions on or about October 1st of each year.

Small Entity

TOTAL FEES AUTHORIZED: \$ 370

The commissioner is hereby authorized to charge indicated processing and/or publication fees and credit any overpayments to:

Deposit Account Number: 01-2950



Deposit Account Name: Stuart T Auvinen

Charge Any Additional Fee Required Under 37 C.F.R. Sections 1.16 and 1.17.

Charge Assignment Fees Required Under 37 C.F.R. Section 1.21 (h).

SUBMITTED BY

Authorized Name: Stuart T Auvinen

Electronic Signature Mark: /e/stuauvinen

Date Signed: 20020306

BASIC FILING FEE

Fee Description	Fee Code	Fee Paid
Utility Filing Fee	201	\$ 370

Subtotal For Basic Filing Fee: \$ 370

EXTRA CLAIM FEES

	Fee Code	Fee	Extra Claims	Fee Paid
Total Claims: 20	203	\$ 9	0	\$ 0
Independent Claims: 3	202	\$ 42	0	\$ 0

Subtotal For Extra Claims Fees: \$ 0